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Paul Chow, P. Glenn Gulak

 February 1995 **Proceedings of the 1995 ACM third international symposium on Field-programmable gate arrays**

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A novel field-programmable mixed-analog-digital array (FPMA) is proposed, which contains a field-programmable analog array, a field-programmable digital array, and a mixed-signal interface. This device is intended to be used for the rapid implementation of mixed-signal circuits. The resource and architectural requirements for this array are determined by analyzing a set of sample circuits. The mixed-signal interface is constructed from converter blocks that contain configurable A/D and D/A ...

### 2 [Pitfalls and safeguards in real-time digital systems with emphasis on programming](#)

W. A. Hosier

 March 1987 **Proceedings of the 9th international conference on Software Engineering**

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Real-time digital systems are largely a technical innovation of the past decade, but they appear destined to become more wide spread in the future. They monitor or control a real physical environment, such as an air-traffic situation, as distinguished from simulating that environment on an arbitrary time scale. The complexity and rapid variation of such an environment necessitates use of a fast and versatile central-control device, a role well suited to digital computers. The usual system w ...

**13. Reconfigurable gate array architectures for real time digital signal processing**

Liersch, G.; Dick, C.;

Signals, Systems and Computers, 1994. 1994 Conference Record of the Twenty-Eighth Asilomar Conference on

Volume 2, 31 Oct.-2 Nov. 1994 Page(s):1383 - 1387 vol.2

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Solid-State Circuits, IEEE Journal of  
Volume 27, Issue 12, Dec. 1992 Page(s):1895 - 1904  
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Mohamed-Nour, H.I.; Pham, L.;  
Conduction and Breakdown in Solid Dielectrics, 1995. ICSD'95.,  
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